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To the attention of: Examiner Benjamin J. Buss
Fax number: 571-273-8300

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> I certify that this correspondence was sent via facsimile to US Patent and Trademark Office at facsimile number 571-273-8300 on December 3, 2007
>
> *[signature]*
>
> David Lewis

Number pages being transmitted including this page _____9_____

Re: Docket #:   11-2
        Serial No: 10/791,249
        Filing Date: March 2, 2004

Dear Examiner Buss,

    Attached is a summary of the interview with Examiner Benjamin J. Buss from November 28, 2007.

    It is believed that no fees are currently due. However, if that is not correct, the Commissioner is authorized to charge any payments that may be due and/or credit any overpayments that may have been made to my deposit account #503345 in connection with this communication.

Respectfully Submitted,

[signature]

David Lewis
Reg. No. 33,101

I certify that this correspondence was sent via facsimile to the United States Patent and Trademark Office at 571-273-8300 on the date of

December____3____, 2007.

David Lewis

David Lewis

Serial Number:	10/791,249
Confirmation Number:	5568
Filing Date:	March 2, 2004
First Named Inventor:	Michael Fiske
Examiner:	Benjamin J. Buss
Art Unit:	2129
Docket Number:	11-2

Title: Effector Machine Computation

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Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Interview Summary

Although not stated during the interview, the Applicant thanks the Examiner for contacting the Applicant's representative and for agreeing to make the Examiner's amendment associated with the present interview.

Examiner Benjamin J. Buss called the Applicant's representative on November 28, 2007, and the Examiner proposed to make the following changes to claims 2, 5, 14, 23, and 60-62, via Examiner's Amendment, in order to place the application in condition to for allowance.

2. (Currently Amended) A hardware computing machine, which will be referred to as an
Effector machine, comprising:

(a) a collection of hardware computing elements, which will be referred to as Effectors,
that are each communicatively coupled to at least one other Effector; and

(b) a machine architecture that

adjusts how the Effectors behave, and

adjusts how information is transmitted from one Effector to another Effector;

wherein a subset of said Effectors is configured to receive information

from a Static program.

5. (Currently Amended) A system comprising a computer readable medium storing
thereon one or more instructions that constitute an input interpreter for designing
at least:

a hardware computing machine, which will be referred to as an Effector machine,

including at least

(a) a collection of hardware computing elements, which will be referred to
as Effectors, that are each communicatively coupled to at least one
other Effector, and

(b) a machine architecture that

adjusts how the Effectors behave, and

adjusts how information is transmitted from one Effector to

another Effector; ~~and~~

the input interpreter outputs a software Effector machine, which is a design for the

hardware Effector machine: and

wherein a subset of said Effectors is configured to receive information from a Static or

Meta program.

14. (Currently Amended) A method comprising designing a machine, at least by evolving

a graph representing the machine to produce a design of the machine,

the machine being a hardware computing machine, which will be referred to as an

Effector machine, including at least

(a) a collection of hardware computing elements, which will be referred to as

Effectors, that are each communicatively coupled to at least one other

Effector; and

(b) a machine architecture that

adjusts how the Effectors behave, and

adjusts how information is transmitted from one Effector to another Effector:

wherein a subset of said Effectors is configured to receive information

from a Static or Meta program.

23. (Currently Amended) A method; comprising: providing a hardware computing

machine, which will be called an Effector machine, by at least

(a) providing a collection of hardware computing elements, which will be referred to as

Effectors,

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(b) communicatively coupling each Effector of the collection to at least one other

Effector;

(c) providing a machine architecture that, while the machine is running,

adjusts how Effectors behave, and

adjusts how information is transmitted from one Effector to another Effector;

the method further comprising designing said machine architecture by at least evolving a

graph associated with the machine architecture;

wherein a subset of said Effectors is configured to receive information from a Static or

Meta program.

Cancel claim 60.

61. (Currently Amended) A method, comprising forming a hardware computing machine

by at least:

(a) providing a collection of hardware computing elements, which will be referred to as

Effectors,

(b) providing a machine architecture that

~~determines~~ adjusts how Effectors behave, and

~~determines~~ adjusts how information is transmitted from one Effector to another

Effector;

(c) configuring each Effector of the collection to be communicatively coupled to at least

one other Effector, and

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(d) configuring a portion of the hardware computing machine for receiving input from a

Meta program that sets values for one or more parameters of individual Effectors

from the collections of Effectors, the one or more parameters including a time at

which information is transmitted from the individual Effectors to another of the

individual Effectors.

62. (Currently Amended) A hardware computing machine, which will be referred to as an

Effector machine, comprising:

(a) a collection of hardware computing elements, which will be referred to as Effectors,

each Effector of the collection being communicatively coupled to at least one

other Effector;

(b) a machine architecture that, while the hardware computing machine is running

~~determines~~ adjusts how the Effectors behave and

~~determines~~ adjusts how information is transmitted from one Effector to another

Effector; and

(c) the hardware computing machine including a portion for receiving input from a Meta

program that sets values for one or more parameters of individual Effectors from

the collections of Effectors, the one or more parameters including a time at which

information is transmitted from the individual Effectors to another of the

individual Effectors.

In response, the Applicant's Representative proposed to add to the end of claims

5, 14, 61, and 62,

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--wherein the Meta program is a program that determines how to change the

Effector machine's architecture as the Meta program executes.--

The Examiner asked whether the two wherein clauses should be part of the same

paragraph, and the Applicant's representative said that either was fine, because he did not

see any difference in the scope of the claim. The Applicant's Representative also

proposed to amend claims 11, 27, 48, 49 54, and 55 as follows.

11. (Currently Amended) The machine of claim 10 wherein the machine is for running a

the Meta program, that which changes, over time, one or more properties

associated with one or more of said Effectors, the Meta program being a sequence

of sets, each set being a list of values of parameters of Effectors, and each list of

values having the parameters,

the machine including at least a portion for receiving the Meta program and for

converting the Meta program into input for that machine.

27. (Currently Amended) The method of claim 26, wherein the dynamic machine is for

running the a Meta program, that which changes, over time, one or more properties

associated with one or more of the Effectors, the Meta program being a sequence of sets,

each set being a list of values of parameters of Effectors, and each list of values having

the parameters in a set order, the machine including at least a portion for receiving the

Meta program and converting the Meta program into input for the machine.

48. (Currently Amended) The machine of claim 37, further comprising an input interpreter for designing at least the a Meta program for the Effector machine, the Meta program being a sequence of sets, each set being a list of values of parameters of Effectors.

49. (Currently Amended) The machine of claim 10 wherein the machine is for running the a Meta program, that which changes, over time, one or more properties of said machine, the Meta program being a sequence of sets, each set being a list of values of parameters of Effectors, the machine including at least a portion for receiving the Meta program and converting the Meta program into input for that machine.

54. (Currently Amended) The method of claim 26 wherein the dynamic machine is for running a the Meta program, that which changes, over time, a threshold associated with one or more Effectors, the Meta program being a sequence of sets, each set being a list of values of parameters of Effectors.

55. (Currently Amended) The method of claim 26 wherein the dynamic machine is for running a the Meta program, that which changes, over time, a refractory period associated with one or more Effectors, the Meta program being a sequence of sets, each set being a list of values of parameters of Effectors.

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The Examiner and the Applicant's Representative agreed to the above proposed changes, and the Examiner offered to correct any other antecedent issues that he should notice via the Examiner's amendment, and the Applicant's Representative agreed.

Although the Applicant's Representative believes the above is an accurate record of the interview summary, it is appreciated that sometimes inadvertent miscommunications occur and sometimes different people have different memories of the same event. Accordingly, if the Examiner notices any omission or misstatement in the above interview summary, the Examiner is invited to contact the Applicant's representative so that the issue may be resolved.

Please feel free to contact the Applicant's undersigned representative at 408-993-1800.

Respectfully Submitted,

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December 3, 2007
Date

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